Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	12	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and multiplex$3 and delay and (data with buses with signal) and (receiv$3 same signal same data same time)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/17 23:42
L2	1	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and multiplex$3 and delay and (data with buses with signal) and (receiv$3 same signal same data same time) and "709".clss.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/17 23:43
L3	0	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and multiplex$3 and delay and (data with buses with signal) and (receiv$3 same signal same data same time) and "711".clss.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/17 23:44
L4	0	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and multiplex$3 and delay and (data with buses with signal) and (receiv$3 same signal same data same time) and "712".clss.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/17 23:44
L5	0	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and multiplex$3 and delay and (data with buses with signal) and (receiv$3 same signal same data same time) and "370".clss.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/17 23:44

S1	13941	((709/203) or (709/204-211) or (709/212-213) or (709/213) or (709/238) or (711/146) or (711/117-146) or (711/147) or (711/167) or (712/10) or (712/32) or (712/203) or (712/245) or (370/211)).CCLS.	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/15 21:54
S2	111524	(("709") or ("710") or ("711") or ("712") or ("370")).CLAS.	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/15 21:55
S3	462	(allen adj james).inv.	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/15 21:55
S4	4	(allen adj james).inv. and snoop$3	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/15 21:55
S5	51	("4325085" | "4494108" | "4929946" | "4973961" | "5014134" | "5097261" | "5109226" | "5138315" | "5225832" | "5272478" | "5276525" | "5302949" | "5313203" | "5319457" | "5357250").PN. OR ("5717394"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 21:58
S6	210798	("4325085" | "4494108" | "4929946" | "4973961" | "5014134" | "5097261" | "5109226" | "5138315" | "5225832" | "5272478" | "5276525" | "5302949" | "5313203" | "5319457" | "5357250").PN. OR ("5717394"). URPN. ans multiprocessor	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 21:58
S7	19	("4325085" | "4494108" | "4929946" | "4973961" | "5014134" | "5097261" | "5109226" | "5138315" | "5225832" | "5272478" | "5276525" | "5302949" | "5313203" | "5319457" | "5357250").PN. OR ("5717394"). URPN. and multiprocessor	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 21:58
S8	15	("4325085" | "4494108" | "4929946" | "4973961" | "5014134" | "5097261" | "5109226" | "5138315" | "5225832" | "5272478" | "5276525" | "5302949" | "5313203" | "5319457" | "5357250").PN. OR ("5717394"). URPN. and multiprocessor and snoop$3	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 21:59

S9	1817	multiprocessor and snoop$3	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 22:00
S10	1023	(multiprocessor and snoop$3)same bus$2	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 22:01
S11	14023	(multiprocessor same system)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 22:02
S12	19551	((multiprocessor or (multi adj processor) or multi-processor) same system)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 22:03
S13	1367	((multiprocessor or (multi adj processor) or multi-processor) same system) same logic	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 22:03
S14	315	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interface$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 22:03
S15	316	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and (microprocessor or processor)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/15 22:04
S16	336	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and (microprocessor or processor)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:05
S17	173	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and ((microprocessor or processor) same clock)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:05
S18	151	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and ((microprocessor or processor) same clock) and ((microprocessor or processor) same first)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:06
S19	142	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and ((microprocessor or processor) same clock) and ((microprocessor or processor) same first) and ((microprocessor or processor) same second)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:06

S20	86	((((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and ((microprocessor or processor) same clock) and ((microprocessor or processor) same first) and ((microprocessor or processor) same second) and ((microprocessor or processor) same third)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:06
S21	36	((((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and ((microprocessor or processor) same clock) and ((microprocessor or processor) same first) and ((microprocessor or processor) same second) and ((microprocessor or processor) same third) and ((microprocessor or processor) same fourth)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:06
S22	14	((((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3) and ((microprocessor or processor) same clock) and ((microprocessor or processor) same first) and ((microprocessor or processor) same second) and ((microprocessor or processor) same third) and ((microprocessor or processor) same fourth) and ((microprocessor or processor) same fifth)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:07
S23	9	((((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock) and ((microprocessor or processor) same first) and ((microprocessor or processor) same second) and ((microprocessor or processor) same third) and ((microprocessor or processor) same fourth) and ((microprocessor or processor) same fifth)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:18
S24	103	((((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:19

S25	58	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock) and multiplex$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:19
S26	38	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock) and multiplex$3 and delay	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:19
S27	37	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock) and multiplex$3 and delay and (data with buses)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:20
S28	31	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock) and multiplex$3 and delay and (data with buses with signal)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:20
S29	29	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and multiplex$3 and delay and (data with buses with signal)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:21
S30	12	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and multiplex$3 and delay and (data with buses with signal) and (receiv$3 same signal same data same time)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/17 23:42

S31	1	(((multiprocessor or (multi adj processor) or multi-processor) same system) same logic same interfac$3 same bus) and ((microprocessor or processor) same clock same memor$3) and (multiplex$3 same delay same (data with buses with signal)) and (receiv$3 same signal same data same time)	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/15 22:23